

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,867,665 B2
APPLICATION NO. : 09/934339
DATED : March 15, 2005
INVENTOR(S) : Manku et al.

Page 1 of 5

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8

Line 26, "end" should read --and--.

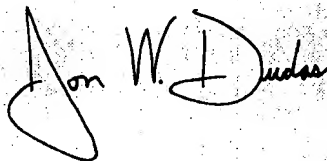
Line 50, "centra" should read --center--.

THE TILTE PAGE, SHOWING AN ILLUSTRATIVE FIGURE, SHOULD BE
DELETED AND SUBSTITUTE THEREFOR THE ATTACHED TITLE PAGE.

DELTE DRAWING SHEETS 1-3, AND SUBSTITUTE THEREFOR THE
DRWAING SHEETS CONSISTIN OF FIGS 1-3 AS SHOWN ON THE
ATTACHED PAGES.

Signed and Sealed this

Third Day of April, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, circular embossed seal of the United States Patent and Trademark Office.

JON W. DUDAS
Director of the United States Patent and Trademark Office

(12) **United States Patent**
Manku et al.

(10) Patent No.: **US 6,867,665 B2**
(45) Date of Patent: **Mar. 15, 2005**

(54) **FILTERS IMPLEMENTED IN INTEGRATED CIRCUITS**

(75) Inventors: **Tajinder Manku, Kitchener (CA); Bish Ray, Kanata (CA)**

(73) Assignee: **Sirflic Wireless Corporation, Waterloo (CA)**

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/934,339**

(22) Filed: **Aug. 21, 2001**

(65) **Prior Publication Data**

US 2002/0047757 A1 Apr. 25, 2002

Related U.S. Application Data

(60) Provisional application No. 60/226,754, filed on Aug. 21, 2000.

(51) Int. Cl.⁷ **H03H 7/00**

(52) U.S. Cl. **333/171; 333/172; 333/176**

(58) Field of Search **333/170, 171, 333/172, 174-176**

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Primary Examiner—Robert Pascal

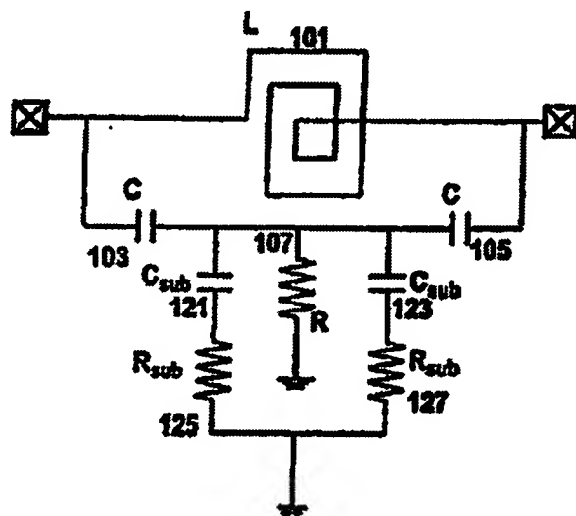
Assistant Examiner—Kimberly Glenn

(74) *Attorney, Agent, or Firm*—Gates & Cooper LLP

(57) **ABSTRACT**

An integrated RF filter for use at microwave frequencies comprising: an integrated circuit inductor with connected integrated circuit capacitors, arranged as a tank circuit, and an integrated circuit shunt resistor; the inductor, capacitors and resistor being interconnected in a bridge-T filter arrangement.

7 Claims, 3 Drawing Sheets



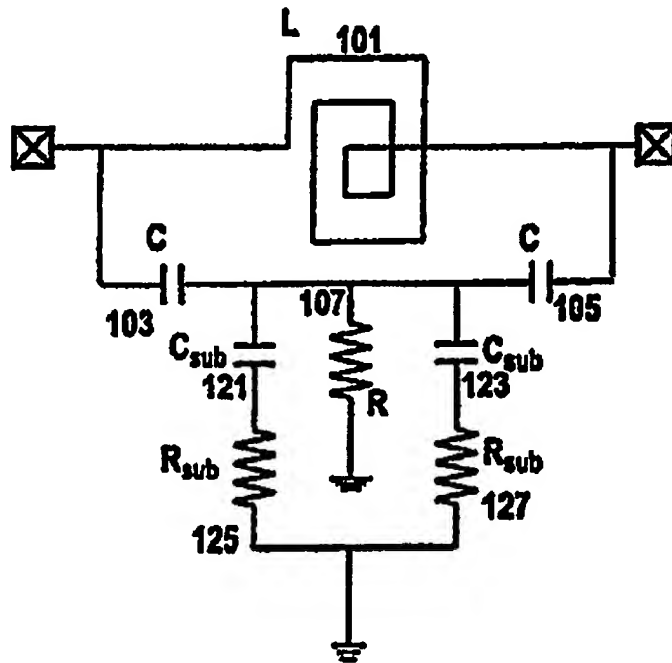


FIG. 1

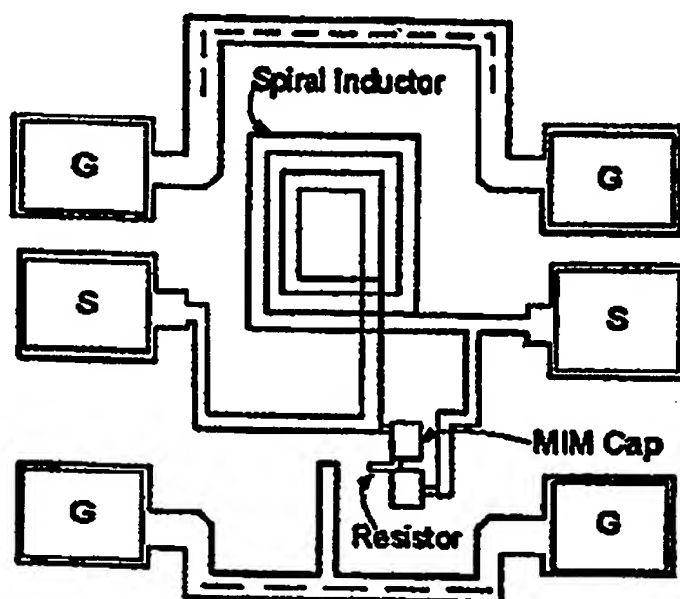


FIG. 2

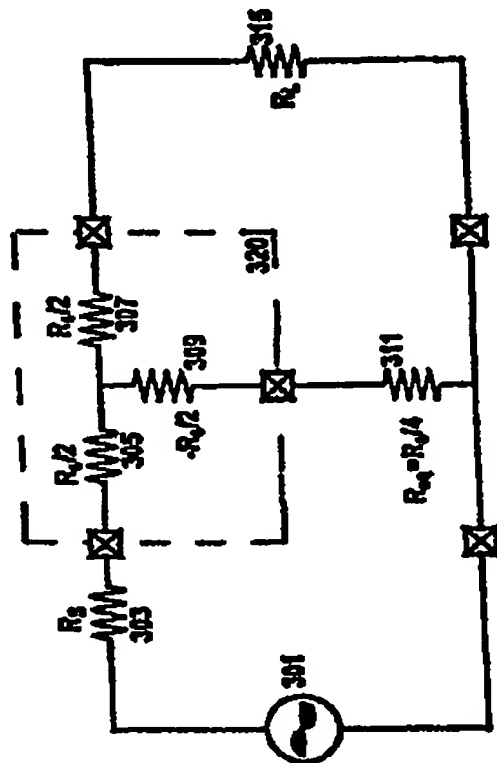


FIG. 3